

Notice of References Cited	Application/Control No. 10/720,611	Applicant(s)/Patent Under Reexamination LIN ET AL.	
	Examiner Heather A. Doty	Art Unit 2813	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,037,228	03-2000	Hsu, Shih-Ying	438/279
	B	US-6,787,415	09-2004	Chung et al.	438/257
	C	US-6,383,921	05-2002	Chan et al.	438/649
	D	US-5,814,563	09-1998	Ding et al.	438/714
	E	US-5,688,713	11-1997	Linliu et al.	438/253
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, R.N. Tauber, Silicon Processing for the VLSI Era, Vol. 1, 2nd Edition, Lattice Press, 2000, pgs. 109-191 and 702-703.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.